COMPUTATION OF THE PERIODIC STEADY STATE IN SYSTEMS WITH NONLINEAR COMPONENTS USING A HYBRID TIME AND FREQUENCY DOMAIN METHODOLOGY

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Abstract - The basic principles of an efficient new methodology for the calculation of the non-sinusoidal periodic steady state in systems with nonlinear and time-varying components are described. All linear parts, including the network and part of the loads, are represented in the frequency domain, while nonlinear and time-varying components, mainly loads, are represented in the time domain. This hybrid process is iterative, with periodic, non-sinusoidal, bus voltages as inputs for both frequency domain solutions and time domain simulations: a current mismatch is calculated at each bus and used to update the voltages until convergence is reached. Thus the process, but not the solution, is decoupled for the individual harmonics. Its efficiency is enhanced by the use of Newton type algorithms for fast convergence to the periodic steady state in the time domain simulations. Potential applications of this methodology are in the computation of a Harmonic Power Flow and in the Steady State Initialization needed in the calculation of electromagnetic transients.

Keywords: Periodic steady state, Harmonic power flow, Initialization for transients, Hybrid solution, Decoupled solution, Newton's method.

1. INTRODUCTION

Nonlinear and time-varying elements are the main source of harmonics in power systems. With a periodic single frequency input, the output will in general contain harmonics of many frequencies. Thus, nonlinear elements (time-varying included) are responsible for having all harmonics coupled in the system. This phenomenon of harmonic coupling is explicitly represented in detailed a.c. models of power network components [1] and of their interaction [2]. Accordingly, a program for the calculation of the non-sinusoidal Periodic Steady State of the system (Harmonic Power Flow) may be of very high dimension [2-6].

The intrinsic harmonic coupling produced by a nonlinear element is itself nonlinear. Only by a *linearization around a particular operating point* is a linear relation between harmonic domain voltages and currents possible [1-2], [9] and it is of course accurate only in a close neighborhood of that point. Not only is the calculation of such a harmonic Norton equivalent computationally difficult but, for accurate results, it has to be iteratively updated. The computational burden is thus further increased in direct proportion with the size of the system and the number of harmonics represented. Nevertheless, improvements in computational efficiency can be achieved by the following actions:

- (a) Replace the harmonic domain calculations for nonlinear elements by direct time domain computations followed by Fourier transforms.
- (b) Decouple the harmonics but recover the accuracy of the coupling by an iterative process.

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Reference [7] has presented results along the ideas outlined above. There the iterative procedure consists of injecting harmonic current phasors into the network and using the resultant voltages, converted to the time domain, as inputs to a nonlinear element yielding a periodic current as output, with all the required harmonic components. This is a fixed point (successive replacement) iteration [8]. The convergence of the process may however be very slow, first, because the network impedance increases with frequency and resonance phenomena may occur [9] and, second, because the time domain simulation for the load must reach the periodic steady state condition of a limit cycle, a process that can take excessive time in the case of lightly damped circuits. Therefore, the present paper incorporates the following features that assure a very efficient overall solution (see Figure 1):

- (1) Fundamentally, frequency domain methods are used for the linear parts where they are computationally most efficient and intrinsically accurate (as in the case of transmission lines with distributed, frequency dependent parameters); and time domain simulation is used for the nonlinear parts (normally loads) where it is the only natural approach.
- (2) The iterative process is not cyclical in the fixed point iteration sense of [7]. Instead, it is based on the calculation of a current mismatch ΔI_h for all harmonics, followed by a voltage update ΔV_h . The mismatch computation is accurate: for the linear part it obtains $I_h = Y_h V_h$ for all harmonics h , and for each load it performs a time domain simulation to obtain the periodic steady state solution $i(t)$, with $v(t)$ as input. The mismatch vector ΔI_h (comprising all load buses) is then used with an appropriate iteration matrix $\tilde{Y}_h$, equal or close to Y_h , to calculate the update increment ΔV_h from $\tilde{Y}_h \Delta V_h = \Delta I_h$. This process always converges.
- (3) The time domain simulation is accelerated by noting that the dynamics of cycles in the neighborhood of a limit cycle is almost linear [11], so that the intercepts with a Poincaré plane can be used to extrapolate to the limit cycle by Newton's Method, possibly with numerical differentiation. The latter has the advantage that it can be applied to non-differentiable nonlinear load characteristics.

The above fundamental principles for the calculation of the non-sinusoidal Periodic Steady State in a system with both linear and nonlinear components are described and validated in this paper. They can be applied to the development of a full-fledged, three-phase Harmonic Power Flow Program or for the particular application of Steady State Initialization needed in an Electro-Magnetic Transients Program (EMTP). It is beyond the objectives of this paper to deal with the details of either application.

Figure 1. The system as seen from the load buses. The voltages V are inputs.

The main problem thus for efficiently finding the limit cycle is the *identification of the B matrix*. In the following we shall present three methods of identification. They all require having initially calculated the base cycle $x(t)$ over a period T , starting from x' ; see Figure 2.

B.1. Matrix Exponential

This approach is based directly on equation (7). After the numerical integration over a cycle, we calculate the matrix exponential in the usual way by resorting to an intermediate similarity transformation of the matrix to diagonal form.

B.2. Direct Approach

The eigenvalue/vector calculation used for the computation of the matrix exponential is not needed if B is calculated directly from (3) as follows. Consider the initial vector $\Delta x'$ to be a column of the identity matrix I . Then, if all columns are considered, (6) gives

$$\Delta X^{t+1} = B \quad (11)$$

Thus, integration of

$$\Delta \dot{x} = J(t) \Delta x \quad (12)$$

with initial vectors $\Delta x'$ being sequentially the columns of the identity matrix I , yields directly the columns of B . Note, that the integration involved here (using for instance a Runge-Kutta method) is more time consuming than the simple numerical quadrature in the Matrix Exponential approach.

B.3. Numerical Differentiation

Both methods described above require the knowledge of the Jacobian $J(t)$. Often J can be calculated analytically but this is not always the case. In particular, in the case of switched devices it is easier to use in (3) the increment Δf rather than $J(t)\Delta x$. This implies that, in addition to integrating (2) with initial value x' for obtaining the base cycle $x(t)$, we also have to integrate it with the perturbed initial value $x' + \varepsilon e^j$, where e^j is column j of the identity matrix I , and ε is a small number. By taking the differences of the two values of x at the end of the cycle, we obtain thus the columns of ΔX^{t+1} of (11), i.e. of εB in this case of down-scaled input. The calculation of B requires m computations as described, just as in the case of the Jacobian matrix.

C - Iteration Strategies

As we have seen, the identification (via B) of the iteration matrix C , which is equivalent to the Jacobian matrix of the Newton process (9), requires $m+1$ computational sequences equivalent each to one cycle of solution of (2). Even though the dynamic order m of the individual nonlinear elements in power systems, considered at present, is usually quite low (often just 1), it is reasonable to consider an iteration matrix alternative where the identification process is performed only once. Then, of course, the convergence to the final limit cycle is only linear, rather than quadratic. The evaluation of these two possibilities will be presented in section 3.

All these computational approaches could be fairly time consuming for large systems. We note, however, that in hybrid calculations the individual nonlinear components are usually of small dynamic order, as noted above, so that the related computational effort is significantly reduced.

D - Linear Loads

Note that linear loads should be represented by admittances. This is equivalent to including them in the bus admittance matrix. As already mentioned, generators are treated in the same way as long as they are modeled as linear elements. This increases the overall efficiency of the computation.

2.2. Calculation of Voltage Updates

To update the voltages we use equation (1) where ΔI_h is the current mismatch vector for harmonic h . Actually, the voltage correction is $-\Delta V_h$ obtained from (1). Ideally, $\tilde{Y}_h$ should be the exact value of the admittance seen from the buses. This, of course, is not just impractical but actually impossible, because nonlinear, as opposed to linear, elements do not respond with a single harmonic output to the input ΔI_h of harmonic h . To make the computations decoupled and simple in terms of harmonics, we represent only a single harmonic h in each of the harmonic domain input/output relations of the nonlinear loads. Consequently, $\tilde{Y}_h$ can only be approximate and the convergence of process (1) is only linear.

2.2.1. Structural Considerations

At power frequency the admittance Y_h of the linear part of the system is strongly dominant. Therefore, we could take $\tilde{Y}_h = Y_h$. At harmonic frequencies, however, the inductive reactance of the network increases and, in addition, capacitive effects further reduce the magnitude of Y_h . Thus, clearly, the effect of the nonlinear parts is not negligible any more. Therefore we have to add to Y_h the correspondent representation of the load at harmonic h . It is however not possible to use a complex admittance matrix for this purpose.

Indeed, a given Y_h means that the angle between ΔV_h and ΔI_h remains the same while the pair can be rotated arbitrarily. In the case of a nonlinear load, however, the terminal power-frequency voltage defines a reference for the zero crossing of all harmonics and the above property no longer exists. Therefore, the voltage/current relation has to be given in terms of real variables. Let

$$\Delta V = \Delta V' + j\Delta V'', \quad \Delta I = \Delta I' + j\Delta I'', \quad \Delta Y = \Delta G + j\Delta B \quad (13)$$

Then the complex equation $Y \Delta V = \Delta I$ becomes

$$\begin{bmatrix} G & -B \\ B & G \end{bmatrix} \begin{bmatrix} \Delta V' \\ \Delta V'' \end{bmatrix} = \begin{bmatrix} \Delta I' \\ \Delta I'' \end{bmatrix} \quad (14)$$

This equation allows rotations. If rotations are not allowed, (14) takes the more general form

$$\begin{bmatrix} G' & -B'' \\ B' & G'' \end{bmatrix} \begin{bmatrix} \Delta V' \\ \Delta V'' \end{bmatrix} = \begin{bmatrix} \Delta I' \\ \Delta I'' \end{bmatrix} \quad (15)$$

Details of the admittance matrix are given in the next section.

2.2.2. Identification of the Real Admittance Matrix

In order to identify the 2×2 real admittance matrix of (15), perform the following computations for a given load:

- Apply a 1 per unit power frequency voltage $\sin \omega_0 t$ and calculate the response $i_0(t)$. Note that this is the *base case* and that it will produce results independent of the final voltage solution as the input is fixed (to 1 p.u.).
- Apply the same voltage with a small perturbation of harmonic h , first in phase, then in quadrature:

$$\sin \omega_0 t + \varepsilon \sin h \omega_0 t \quad (16a)$$

$$\sin \omega_0 t + \varepsilon \cosh \omega_0 t \quad (16b)$$

and calculate the respective responses, $i_h'(t)$ and $i_h''(t)$. Take the differences with respect to $i_0(t)$ and denote them as $\varepsilon \Delta i_h'(t)$ and $\varepsilon \Delta i_h''(t)$.

- (c) Calculate the Fourier coefficients of sine- and cos-terms of order h of the output increments $\Delta i_h'(t)$ and $\Delta i_h''(t)$. Since these correspond to 1 p.u. incremental sine- and cos-voltages in (16), they represent admittances and can be identified with the elements G', B' and G'', B'' of the coefficient matrix in (15), as follows:

- ♦ For sine-input: $\sin/\sin \rightarrow G', \quad \cos/\sin \rightarrow B'$
- ♦ For cos-input: $\sin/\cos \rightarrow -B'', \quad \cos/\cos \rightarrow B''$

3. VARIANTS OF IMPLEMENTATION

The principles of the general methodology described in previous sections have been applied to the development of a single-phase a.c. Hybrid Harmonic Power Flow program. Different implementation strategies were explored for the modeling and solution of the linear and nonlinear parts, in order to investigate their relative merit. Details of the experience obtained throughout this investigation are given in the following sections.

3.1. Strategies for the Time-Domain Acceleration Process

The numerical procedures described in section B above have quadratic convergence. Thus, when applied to the brute force approach, fast convergence to the limit cycle will be achieved. To minimize the number of repetitive applications of a speed-up method and to better exploit its convergence properties, the algorithm should be first applied once the intercepts with the Poincaré Map can be approximately represented by the linear equation (8). In other words, a number of full time-domain cycles are first run, thus allowing the initial transient to settle down. The number of full cycles initially required depends on the system characteristics, in particular load damping. In the authors' experience, 3 to 4 are sufficient for a well damped system and 6 to 7 for a system with light damping.

Each application of a time domain acceleration procedure can be visualized as a "jump" from a reference intercept x' with the Poincaré plane to reach the starting point of the limit cycle, theoretically obtained by brute force alone only after an infinite time. In practical terms, the state variables at the limit cycle are calculated to an accuracy defined by a specified tolerance for convergence. In all test studies to be presented, a criterion for convergence of state variables of 10^{-10} p.u. was used. The computational efficiency to achieve convergence is directly dependent on the strategy followed for the time domain computations. The following algorithmic alternatives have been implemented in order to analyze and compare their potential advantages and drawbacks.

3.1.1. Full NR/ND Procedure

This algorithm is based on the repetitive application of the ND (Numerical Differentiation) or NR (Newton-Raphson of section B.1 or B.2) procedure during the brute force speed-up process, until a criterion for convergence is satisfied. The approach exploits to the full the quadratic convergence properties of these algorithms. The iteration matrix B is calculated for each application of the NR/ND method. In order to obtain all the columns of B , the procedures described in sections B.2 and B.3 require a minimum number of full time domain cycles equal to the number of state variables. On the other hand, the matrix exponential approach would involve the computations required for the evaluation and numerical integration of the Jacobian along the base cycle, followed by an eigen-analysis process [15] to obtain B of (7) as

$$B = V e^{\Lambda} V^{-1} \quad (17)$$

where

V : matrix of eigenvectors

Λ : diagonal matrix of eigenvalues

By way of example, the three-bus system of Figure 3 has been solved by the brute force approach and by applying the acceleration techniques of section B above. Seven state variables are to be calculated. Table 1 summarizes relevant information obtained from the application of each procedure, in terms of the number of full time domain cycles (NFC) required for convergence to the limit cycle and maximum error in state variables (in per unit).

Figure 3. Three-bus test system

NFC	Brute Force	ND method	Direct NR
8	0.07845	0.07845	0.07845
16	0.02347	0.03536	0.03536
24	0.00610	5.3124e-05	5.3133e-05
32	0.00159	5.4396e-11	7.6770e-12
⋮	⋮		
162	9.7602e-11		

Table 1. Errors during convergence of time domain methods

Despite the light damping assumed in the system, three consecutive applications of any of the speed-up algorithms were sufficient to satisfy the specified criterion for convergence. The limit cycle is obtained within this precision. In all cases, before applying the acceleration procedure, the brute force approach was run for seven initial cycles. An additional full cycle is required for the purpose of obtaining the base cycle. It should be noted that a good base cycle is essential to minimize the number of repetitive applications of a speed-up procedure. For this system, a minimum of eight full cycles are required for each NR/ND application.

The ND method has been found to be faster than the NR approach; this is because of its simpler and straightforward formulation. Note from Table 1 its powerful and reliable convergence characteristics. It differs from NR only because of the round-off error in numerical differentiation.

The approximate CPU times for the Brute Force, ND and Direct NR procedures were 35.1, 6.8 and 7.9 seconds, respectively. Each full time domain cycle in Table 1 took approximately 0.21 seconds. Measured CPU times are of course highly dependent on the computer system (a 64 bit KSR1 computer was used in this investigation) and individual code efficiency.

3.1.2. Partial NR/ND Procedure

The purpose of this solution scheme is to reduce the number of full time domain cycles and computation time, required by the full NR/ND method to lead the brute force approach to convergence at the limit cycle. This however is at the expense of degrading the natural convergence properties of the NR/ND method.

The algorithm can be described as follows: an initial estimate of x^* is first obtained with (8) with the application of the NR/ND method to the brute force procedure; the iteration matrix B is kept constant for a number of subsequent evaluations of x^* , then recalculated to start again this cyclic process until convergence to the limit cycle is achieved. A variant of this approach would consist in resuming the process described in section 3.1.1 once B has been recalculated at a particular solution stage.

Table 2 illustrates relevant information obtained with the application of this solution procedure. After the first NR/ND application, the same matrix B was used for the next two evaluations of x^* , yielding the state variable errors of cycles 17 and 18. With a second NR/ND application the solution is brought very close to the limit cycle. At this stage B is very accurate: it has allowed the next estimation of x^* at the limit cycle to be done with very high precision, in just one additional time domain cycle.

NFC	Partial ND method	Partial NR method
16	0.03536	0.03536
17	3.6742e-03	3.6740e-03
18	2.2358e-04	2.2357e-04
26	8.9812e-10	8.4602e-10
27	3.1090e-14	7.6600e-15

Table 2. Errors during convergence of partial NR/ND procedures.

3.1.3. Single NR/ND Application

This solution method is based on a single application to the brute force approach of a full NR/ND procedure. The matrix B is used to obtain the first estimate of x^* and kept constant for further evaluations of x^* . Convergence to the limit cycle requires more applications of (9), as compared with those needed by algorithms of sections 3.1.1 or 3.1.2. However, fast updating of x^* is achieved with this approach, since the repeated identification process of matrix B is avoided. This procedure is an attractive alternative when the initial base cycle lies close to the limit cycle. Table 3 illustrates the results obtained with this approach of linear convergence.

NFC	Single ND method	Single NR method
18	2.2358e-04	2.2357e-04
19	1.7269e-05	1.7267e-05
20	1.7636e-06	1.7635e-06
⋮	⋮	⋮
24	1.9203e-10	1.9205e-10
25	3.4268e-11	3.4270e-11

Table 3. Errors during convergence of single NR/ND procedures.

3.2. System Admittance Matrix Representation

The system admittance matrix Y_h , defined in section 2, can be represented, for the iterative solution of (1), according to any of the following procedures.

3.2.1. Full System Admittance Matrix

The linear components and loads are represented as admittances and included directly into Y_h . The nonlinear effect of loads is not taken into account in Y_h and, therefore, the identification process of section 2.2.2 and the related time domain calculations are avoided. This is an attractive alternative for the solution of systems with low or moderate harmonic distortion, as it considerably speeds-up the iterative hybrid solution.

3.2.2. Adding the Effect of Non-Linear Loads

The elements of the real admittance matrix, obtained with the identification process of section 2.2.2, are incorporated into Y_h .

This process strengthens the robustness of Y_h , thus resulting in a significant reinforcement of the natural convergence properties of the hybrid method in systems with high harmonic distortion or where a resonance condition is present. Besides, we have observed that under such circumstances, the number of iterations required for the convergence of the hybrid algorithm can be significantly reduced, compared to those needed if Y_h does not include the effect of nonlinear loads.

In our experience, the calculation of the Fourier coefficients of section 2.2.2(c) is usually necessary for a limited number of harmonics, e.g. the first 11 harmonics. This considerably reduces the time domain calculations involved for their determination. Exception is made for the case when a higher order resonance is present in the system, in which case the coefficients should be calculated up to that particular harmonic. Furthermore, note that in the absence of d.c. excitation, (1) needs to be solved only for odd harmonic orders. Thus, the computation time for the iterative hybrid solution process is halved.

3.2.3. Decoupled Admittance Matrix Method

Taking advantage of the structure of Y_h , the principles of decoupling [16] can be applied. Thus, setting $G' = G'' = 0$ in (15) yields the decoupled equations

$$-B'' \Delta V'' = \Delta I' \quad (18a)$$

$$B' \Delta V' = \Delta I'' \quad (18b)$$

The effect of nonlinear loads can be appropriately incorporated into B' and $-B''$ of (18). To preserve high accuracy in the computation of $\Delta I'$, $\Delta I''$ and $\Delta I'''$ are calculated without decoupling in Y_h .

It can be mentioned among the computational advantages of this approach, that B' and $-B''$ are half the original size of Y_h and are symmetrical and sparse. It should however be noted that decoupling of (15) generally results in an additional number of iterations to achieve convergence, in our experience an average of 2 in systems with low harmonic distortion. In networks with high harmonic distortion this technique should be used with caution, as it would require a significant number of additional iterations to obtain convergence, which itself cannot be guaranteed in these cases even with the incorporation of the susceptance terms of the nonlinear loads.

3.3. Achieving Network Admittance Symmetry

Equation (15) details the real formulation of (1). Note that the admittance matrix of (15) is asymmetrical, with off-diagonal elements dominant in transmission networks. Symmetry of the admittance matrix can however be achieved by a simple procedure of reordering of rows/columns. Thus, reordering of rows in (15) yields the matrix equation

$$\begin{bmatrix} B' & G'' \\ G' & -B'' \end{bmatrix} \begin{bmatrix} \Delta V' \\ \Delta V'' \end{bmatrix} = \begin{bmatrix} \Delta I' \\ \Delta I'' \end{bmatrix} \quad (19)$$

In (19) $G' = G''$ and $B' = B''$ only for the linear part of the system. For the nonlinear part, we set $G', G'' = 0$. Thus, we include only the susceptances representing the nonlinear effect of loads to preserve the symmetry of the admittance matrix. This did not negatively affect the original convergence of the voltage correction process based on (1).

Symmetry in the admittance matrix allows sparsity techniques to be applied to the lower triangular matrix only. Following the procedure presented, the storage and computation time required for the ordering, factorization and solution of (19) is nearly halved.

3.4. System Components

The basic a.c. network components can be represented in the hybrid method as follows.

3.4.1. Transmission Lines

A transmission line can be represented either by a simple lumped parameter model or by a model for long-line effects. In the former case, the series impedance and total shunt admittance of a transmission line at a frequency ω are

$$Z = R + j\omega L \quad (20a)$$

$$Y = j\omega C \quad (20b)$$

The shunt admittance Y is halved and placed at both ends of the transmission line. The corresponding long-line parameters of the transmission line are

$$Z_\omega = Z_c \sinh \Gamma \quad (21a)$$

$$Y_\omega = \frac{2}{Z_c} (\cosh \Gamma - 1) \quad (21b)$$

where

$$Z_c = \sqrt{Z/Y} \quad (22a)$$

$$\Gamma = \sqrt{ZY} \quad (22b)$$

Note that Z_ω and Y_ω are equal to the original Z and Y only if $\omega \rightarrow 0$. Equations (21) accurately represent the transmission line at harmonic frequencies. On the other hand, it has been observed that the damping term of Y_ω strengthens the convergence properties of the hybrid method. A reduction by one iteration to achieve convergence was generally noted with the use of this formulation.

3.4.2. Generators

In harmonic analysis of electric networks, generator units can be represented either as constant source rigidly fixed to an infinite bus or as an internal *emf* behind a generator reactance. Both models have been implemented and analyzed. The infinite bus model is given by the equation

$$V_i = |V_i| \angle \alpha_i \quad (23)$$

The phase shifts α_i are obtained from a load flow solution.

The internal generator *emf*, E_i , can be obtained by post-processing following the load flow solution. For the computation of harmonics, this results in additional buses and branches, equal in number to the generator units existing in the system. It is more convenient to add the created *emf* nodes at the end of the bus list: this makes the computational process more efficient and straightforward, as any complication related to bus reordering is avoided. In addition, real powers originally at generator terminals are now transferred to the internal generator nodes; rigid buses of controlled voltage, slack or *PV*, now become "soft" load buses (*PQ*).

The solution of a power network with the hybrid technique, using either of both generator models previously described, will yield very accurately the original load flow solution, plus the harmonics. The small deviation that will be observed with the application of the internal *emf* model is produced by two facts: the now allowed voltage variation of the created "soft" buses, and the cross-coupling between harmonics. Higher harmonic content is obtained with this model, as the generators' *emf*'s are now driving the system.

3.4.3. Nonlinear Loads

These system components are modeled in the time domain, with the differential equations solved by the *R-K* integration method.

Convergence of this process is accelerated by any of the procedures described in section 3.1. Different degrees of nonlinearity have, for example, been simulated with saturable reactors of different sizes and levels of saturation.

The following strategy for time domain solution has been found to work satisfactorily for the hybrid method: use the algorithm of section 3.1.1 for the first two iterations, then switch to the approach of section 3.1.2 and once the iterative process is close to convergence use the procedure of section 3.1.3. Significant saving in computation time can be achieved with the reduction of full time domain cycles. It should be noted, however, that in the presence of significant harmonic distortion or resonance in the system, the use of the procedure of section 3.1.1 throughout the iterative process can result in fewer full time domain cycles.

3.5. Sparsity

Efficient computations with the hybrid approach are achieved with the incorporation of sparsity techniques. Dynamic ordering on Y_k is based on the scheme of minimal degree [17]. Symmetry of Y_k is restored as shown in section 3.3. Since there is no change of topology during the iterative solution, ordering and factorization of Y_k are carried-out only once, at the first iteration, and the resulting factors are used throughout the iterative process.

4. TEST CASES

The test system of Figure 3 has been used for the purpose of comparison between a pure time domain method and the proposed hybrid harmonic load flow method. Light damping is assumed in the system. The time domain convergence tolerance for state variables was 10^{-10} p.u. and the criterion for convergence of the full hybrid process was 10^{-7} p.u. In the hybrid approach the iterative process is completed once either the current mismatches or the voltage corrections satisfy the convergence tolerance. Convergence was obtained in 12 and 6 iterations by using the representations of Y_k of sections 3.2.1 and 3.2.2, respectively.

Even for the small test system used here for illustration, the full time domain solution required at least one order of magnitude more time than the hybrid solution. For any of the realistic power systems of section 4.2, a full time domain solution would not have been feasible in a reasonable amount of time and a frequency domain solution has complex modeling problems in the representation of the nonlinear and time-varying components.

4.1. Time Domain Solutions and Hybrid Technique Validation

The converged solutions obtained with the brute force approach or the accelerated time domain solution, applying the ND and direct NR method, are identical. In our studies agreement at least to the 8th digit has been obtained. Details of maximum state variable error and full time domain cycles required for convergence are given in Table 1.

The new hybrid methodology for harmonic analysis of power systems has been successfully validated against solutions given by the different time domain approaches discussed previously. Excellent agreement has been obtained.

By way of example, Table 4 illustrates the solution obtained at bus 3 of the test system of Figure 3 with various time domain procedures and the hybrid method. All gave identical results. Only a few harmonics h are shown in the table, obtained by Fourier analysis of the voltage waveform.

h	1	3	5	7
V_h	0.98138	3.8687e-02	3.5543e-03	8.0010e-04

Table 4. Voltage harmonics obtained by different methods.

4.2. Application to the Solution of Larger Systems

The hybrid method has been successfully applied to obtain the periodic steady state solution of larger systems. The IEEE-14, 30, 57 and 118 Bus Test Systems [18] have been solved with this technique. By way of example, Table 5 illustrates the harmonic solution for three particular buses of the 118 bus test system where a nonlinear load has been connected. This system contains 53 generator units and, in addition, 12 nonlinear loads were assumed in the system. At each bus where a nonlinear load is placed, 90% of the total load power is assumed to be linear and 10% nonlinear. Convergence was achieved in 4 iterations. The generator units were represented with the *emf* source model.

Harmonic	Bus 7	Bus 101	Bus 118
1	0.98913	0.99158	0.95101
3	2.6378e-03	2.3056e-03	1.7861e-03
5	3.2945e-05	1.2651e-04	6.0597e-05

Table 5. Some bus harmonic voltages, IEEE 118 Test System

5. CONCLUSIONS

A new algorithm for the computation of the periodic steady state of power systems has been presented. It is based on a hybrid time and harmonic domain methodology. A hybrid harmonic power flow program has been developed using the principles of this methodology.

In the hybrid approach, harmonics are solved independently. However, the effect of cross-coupling between harmonics is restored with the accurate calculation of the current mismatch at buses with nonlinear loads.

The proposed hybrid method has good convergence properties. Convergence has generally been obtained in less than ten iterations, even in the presence of significant harmonic distortion or resonance in the system. When required, the convergence characteristics of the algorithm can be reinforced with the incorporation, into the admittance matrix, of the effect of nonlinear loads. The linear network is solved with the application of sparsity techniques and of a procedure to restore the symmetry of the admittance matrix.

The hybrid harmonic power flow methodology has been successfully validated against time domain methods. Its application to the solution of larger electric networks has been described.

Efficient methods for the acceleration of convergence have been described, implemented and analyzed. The importance of their application for fast time domain computations has been clearly demonstrated. Strategies for the application of methods for brute force convergence speed-up have been detailed, their merits and drawbacks analyzed.

The hybrid methodology presented in this paper is general and can be applied to the periodic steady state solution of three-phase systems. A forthcoming paper will report on the investigation in this area.

6. ACKNOWLEDGMENTS

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BIOGRAPHIES

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